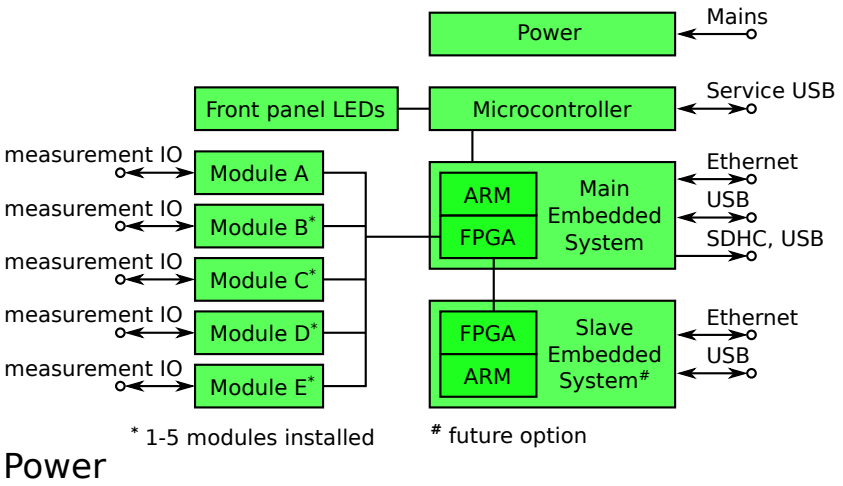
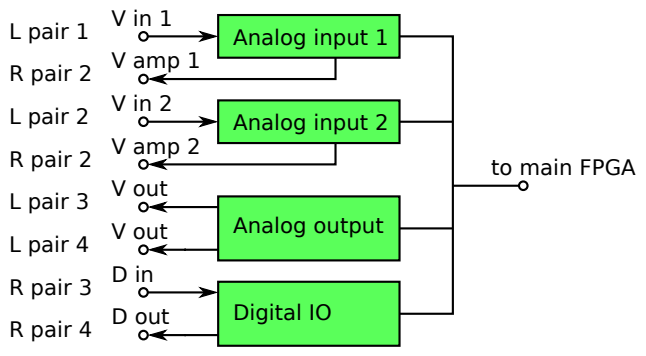
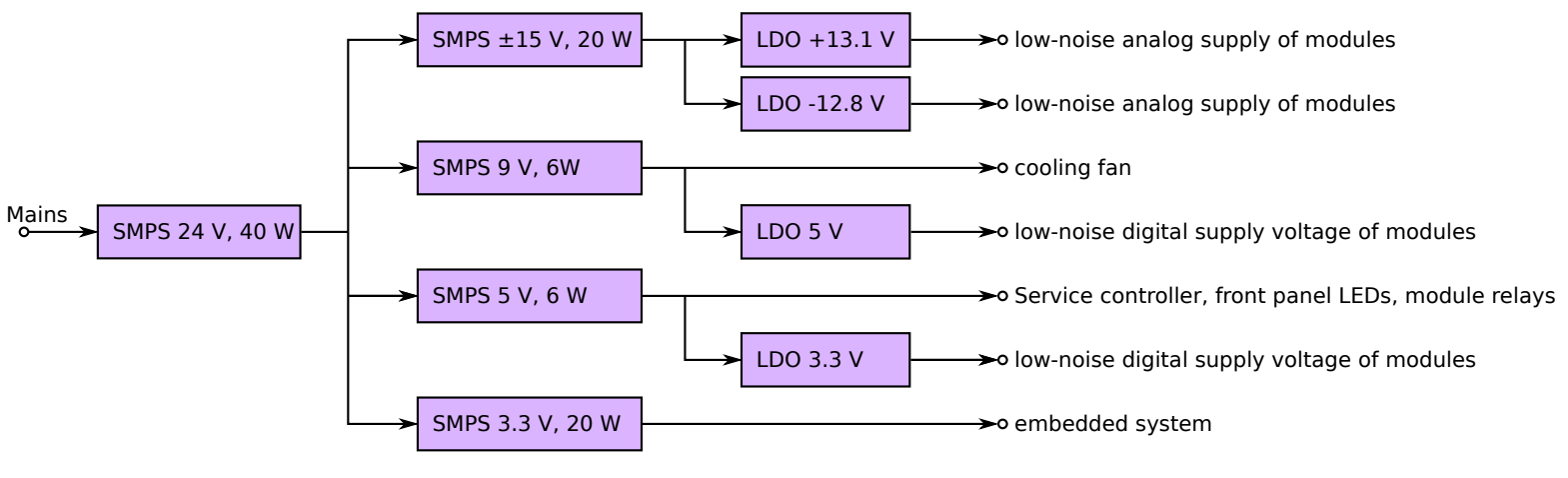
System block diagram



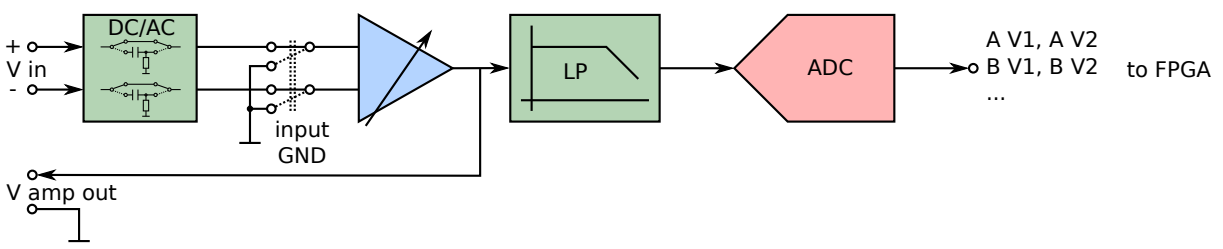
Module A-E



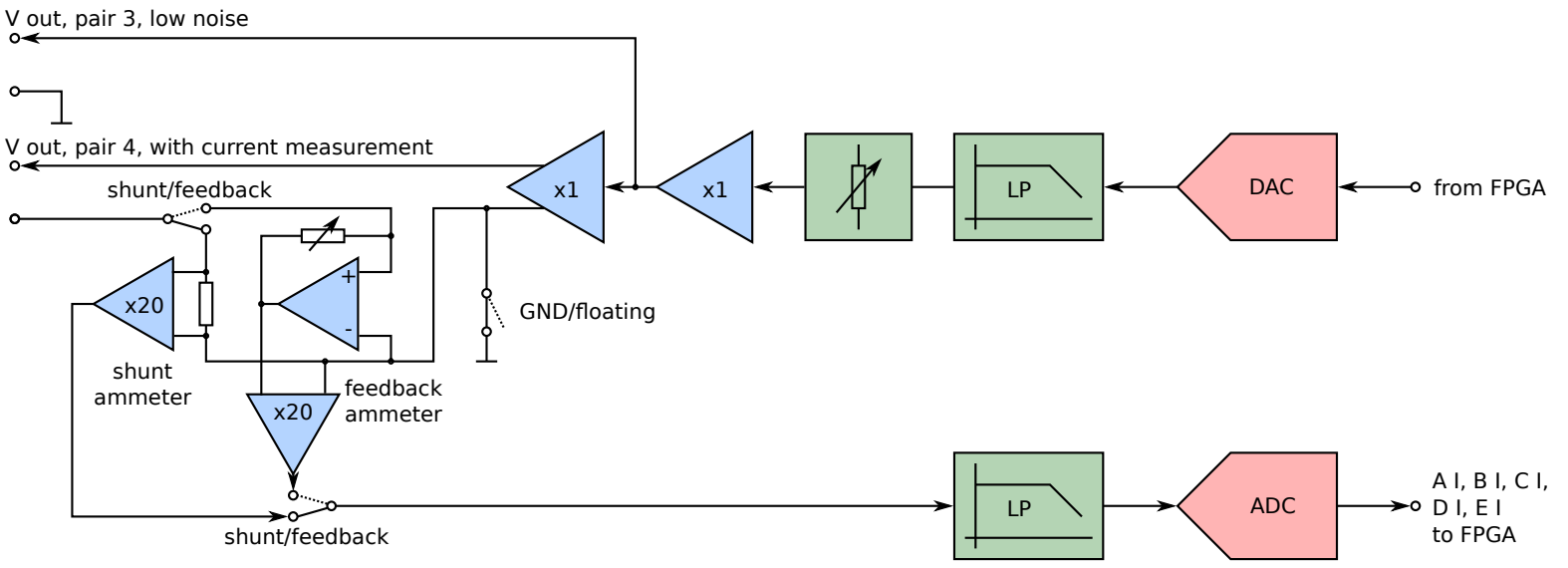
Power



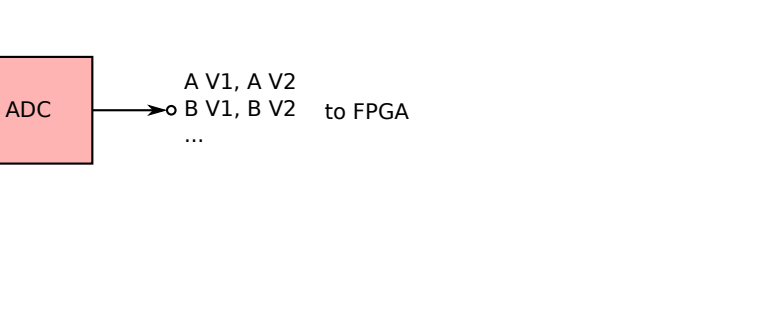
Module analog input 1 & 2



Module analog output



FPGA



Lock-in

x2 sets A, B
x15 V_{A1}...V_{E2}, I_A...I_E